



Guru Gobind Singh Indraprastha University
“A State University established by the Govt. Of NCT Delhi”
Sector 16-C, Dwarka, New Delhi – 110078



F. No.: GGSIPU/CCGPC/2023/ 764

13th October 2023

Sub. Placement opportunity for B.Tech CSE and ECE students of USICT, GGSIP University of the batch passing out in year 2024 in “C-DOT (Centre for Development of Telematics)”.

Dear Placement Officer,

Greetings from CCGPC, GGSIPU!!!

Please find below details of Placement opportunity for B.Tech CSE and ECE students of USICT, GGSIP University of the batch passing out in year 2024 in “C-DOT (Centre for Development of Telematics)” for your reference and circulation to students to apply on given link by **27th October 2023**.

Name of Organisation - C-DOT (Centre for Development of Telematics).

It is a premier R&D institute of the Government of India, engaged in Research & Development of various telecom technologies.

This year C-DOT is planning to conduct Campus interviews for recruitment of final year B.Tech / M.Tech students with specialization in Electronics & Communications (ECE) and Computer Science (CSE) (**Online/Offline**) mode for the post of Research Engineer.

ROLE TITLE : RESEARCH ENGINEER

The eligibility criterion is as follows:

1. Eligibility: B.Tech/M.tech students with specialization in Electronics & Communications (ECE) and Computer Science (CSE) who have Aggregate of all semesters in B.Tech & M.Tech 70% or 7 CGPA (on 10 point scale)

- Linear conversions will be done if any other scale is followed
- In case of M.Tech/M.E., the above marks criteria shall apply to both M.Tech/M.E & B.Tech/B.E. qualifications.
- The above is upto and inclusive of the 6th semester (3rd year).
- However appointment will be subject to concurrence to the above marks after completion of final semester.
- Every exam should be cleared in first & single attempt. **There should not be any failure and repeat. (no backlog in any semester)**
- 70% or 7 CGPA in 10th and 12th. Both exam should have been cleared in first & single attempt.
- Candidate should be **Indian National** only.
- Training & induction cost of Rs 1 lakh will be recovered if the candidate leaves during probation period of 1 year.

2. Age Criteria:

- GEN/EWS CATEGORY : Not exceeding 25 years as on 1st August, 2024.
- OBC CATEGORY : Not exceeding 28 years as on 1st August, 2024.
- SC/ST/PH CATEGORY : Not exceeding 30 years as on 1st August, 2024.

Package – The CTC offered by C-DOT for B.Tech/M.Tech would be approximately **Rs. 18.50 lakhs** per annum.

Please note that the candidates must meet the above criteria with respect to **marks and age both** at the time of appearing for campus interviews & final placement in C-DOT (after the completion of the course).

Registration Process –

Students can login and register themselves in C-DOT Recruitment portal for the process.

The link for the portal is: https://cdotrecruitment.cdote.in/college_login

College login id is	GGSIU
password is	GGSIU#2024

After login through the registered email id, the candidates are requested to fill-in the required details and save the profile at every stage and after uploading necessary documents check the correctness under print preview before finally submitting it. After submitting there is no scope for any kind of changes in the profile. (**Name should be entered as per 10th Certificate**)

THE LAST DATE FOR STUDENTS TO UPLOAD THE APPLICATION IN C-DOT PORTAL IS 27.10.2023.

Please note if the number of eligible students as per above criteria exceed 30 in number, C-DOT will be shortlisting the candidates and final shortlisted candidates will be informed to attend the interview. With regard to shortlisting C-DOT decision will be final and no communication in this regard will be entertained.

Please find attached JDs for more information.

LAST DATE FOR REGISTRATION IS 27th October 2023.



(Dr. Nisha Singh)
Training and Placement Officer,
CCGPC, GGSIP University

ROLE TITLE : RESEARCH ENGINEER (ECE)

KEY ACCOUNTABILITIES:

- Understanding Product Requirements, Architecting a suitable solution after an analysis of different alternatives
 - Design and/or testing of a board, including PCB design in CAD tool environment. The board would typically be a microprocessor based board, with a variety of electrical and optical network interfaces, and could involve RF/DSP/ Fibre-Optical system components/network processors etc.
 - Design, coding, simulation and testing of FPGA/PLD
 - Testing of the design and design related procured components e.g. optical modules, amplifiers, couplers etc.
 - Validation of a product/solution and ensuring its working in the customer environment
 - Documentation of the design , to facilitate mass production of the design and technology transfer

Role Requirement Behaviors:

Team Spirit, proficiency towards using latest hardware design and testing tools.

ROLE TITLE : RESEARCH ENGINEER (CSE)

KEY ACCOUNTABILITIES:

- Well versed with computer network and system programming having excellent software development skills and problem tackling approach
- sound base in computer design and architecture
- Basic knowledge in RISC, CISC architecture types, and variants of processors based on Intel, MIPS and PowerPC
- In depth knowledge in operating system concepts viz; memory management, disk scheduling, multithreading, parallel processing, use of semaphores and tackling different deadlock situations, ways to detect and avoid them optimally
- Developer having decent knowledge in database concepts and normalization, having concrete idea about use of ER diagrams
- Expertise in C/C++/ Python/MEAN stack programming and sound knowledge in optimum algorithm and design patterns
- Expertise in Objective C & Swift is desirable
- Exposure in J2SE,J2EE, web technologies and Linux environment is desirable
- Exposure to Basic Data Science, NLP, Machine Learning, Deep Learning (CNN,RNN,LSTM) is desirable

Role Requirement Behaviors:

Analytical Thinking, Learning Ability, teamwork, continuous learning